| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 1 | ((reconfigurable adj device) and tiles and (interconnect adj architecture) and switches and registers and route and latch and (repeating adj time adj period)).clm. | US-PGPUB | OR | ON | 2005/09/29 14:54 |
| L2 | 0 | ((reconfigurable adj device) and tiles and (interconnect adj architecture) and switches and registers and latch and (data adj transport adj segment)).clm. | US-PGPUB | OR | ON | 2005/09/29 14:54 |